



THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

NANJO, Ryota et al.

Serial No.: 10/084,367

Filed: February 28, 2002

Group Art Unit: 2822

Examiner: Mark V Prenty

P.T.O. Confirmation No.: 9203

For: SEMICONDUCTOR DEVICE AND ITS MANUFACTURE METHOD

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
Washington, D.C. 20231

November 26, 2002

Sir:

This is in response to the Office Action of October 29, 2002, requiring restriction between two alleged inventions under the provisions of 35 USC § 121.

In the Action, the examiner made a restriction requirement between the inventions of **Group I** drawn to a semiconductor device (claims 18-20); and **Group II** drawn to a method of making a semiconductor device (claims 1-17 and 21-24).

Applicants hereby provisionally elect **Group II, that is, claims 1-17 and 21-24**, for examination on the merits in this application. Applicants reserve the right to file one or more divisional applications directed to the subject matter of the non-elected claims.

Favorable consideration of the subject application is respectfully requested.

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In the event this paper is not timely filed, the undersigned hereby petitions for an appropriate extension of time. The fee for this extension may be charged to Deposit Account No. 01-2340, along with any other additional fees which may be required with respect to this response.

Respectfully submitted,

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